

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Inventors: Sheng Teng Hsu, and Yoshi Ono

Serial No: 10/622,667

Filed: July 17, 2003

Title: LOW POWER FLASH MEMORY
CELL AND METHOD

PATENT
APPLICATION

Attorney Docket No.
SLA0560

Group No. 2811

Examiner:
Prenty, Mark V.

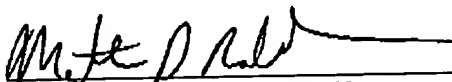
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I hereby certify that this correspondence is being facsimile transmitted to the
United States Patent and Trademark Office at Fax No. (703) 872-9306 on July 26, 2004


Matthew D. Rabdau, Reg. No. 43,026
Signature Date: July 26, 2004

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This communication responds to an Office Action dated
July 23, 2004 in the above identified patent application. In the Office

Action the Examiner requires restriction to one of the following groups:

- Group I. Claims 9-11, drawn to a semiconductor device, classified in class 257, subclass 321;
- Group II. Claim 1-8, drawn to a method of making a semiconductor device, classified in class 438, subclass 257.

Applicants hereby elect to pursue the Group II claims, claims 1-8, drawn to a method of making a semiconductor device.

Respectfully submitted,

Date: July 26, 2004

By: Matthew D. Rabdau
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